

Notice of References Cited	Application/Control No. 10/002,060	Applicant(s)/Patent Under Reexamination CABOT ET AL.	
	Examiner J. Derek Rutten	Art Unit 2122	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-3,700,869	10-1972	Low et al.	708/252
	B	US-5,539,907 A	07-1996	Srivastava et al.	717/130
	C	US-5,768,500 A	06-1998	Agrawal et al.	714/47
	D	US-6,070,009 A	05-2000	Dean et al.	717/130
	E	US-6,167,766 B1	01-2001	Dunn et al.	73/863.01
	F	US-6,718,286 B2	04-2004	Rivin et al.	702/186
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. McCanne and C. Torek, "A randomized sampling clock for CPU utilization estimation and code profiling", 1993, Proc. of Winter USENIX Conference, (San Diego, California), < http://citeseer.ist.psu.edu/mccanne93randomized.html >
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.